

# Enhancing Testing of VDM-SL models

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**Kenneth Lausdahl<sup>3</sup>**



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# Agenda

Problem/motivation

Unit/integration testing in VDM

VDMUnit and code-generation extensions

Conclusion and future plans

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- Testing SL models
  - Tedious and error-prone
  - Lack of tool support
- VDM++/VDM-RT is supported by VDMUnit
- VDMUnit does not work for VDM-SL
  - Relies on features not available in VDM-SL
    - Object-orientation and exception handling

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- Improve unit/integration testing in VDM-SL
  - by extending VDMUnit
- Reuse model tests to validate model realisation
  - by extending Overture's VDM-to-Java code-generator
- Improve continuous integration in a VDM setting
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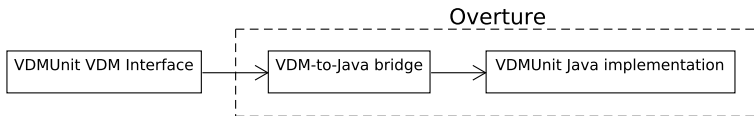
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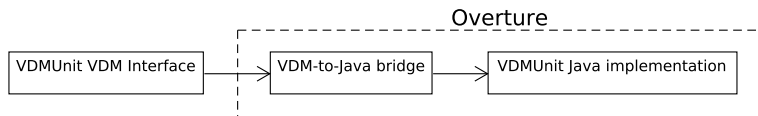
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# VDMUnit architecture



- Consists of a *VDM* and a *Java* component
  - *VDM*: exposes VDMUnit features
  - *Java*: identifies/executes tests using Java reflection
- *VDM* and *Java* connected via *Overture's Java bridge*

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  - VDM: exposes VDMUnit features
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# VDMUnit tests in VDM++

```
class MyTest is subclass of TestCase
operations
public testOne : () ==> ()
testOne () == Assert `assertTrue("Expected `someFeature` to generate " ^
  "an even number ", someFeature() mod 2 = 0);
end MyTest
```

- Test classes extend `TestCase`
  - Test operations begin with “test”
  - Features validated using assertions
- `setUp` and `tearDown` invoked before/after each test
- Tests execution: all vs. selectively
  - All: `new TestRunner.run()`
- *OO features not available in VDM-SL*

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# VDM-SL modules expose VDMUnit features

```
module Assert
imports from TestRunner all
exports all

definitions

operations

assertTrue: bool ==> ()
assertTrue (pbool) ==
  if not pbool then
    TestRunner`markFail();

assertTrueMsg: seq of char * bool ==> ()
assertTrueMsg (pmessage, pbool) ==
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module TestRunner
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run : ()==>()
run() == is not yet specified;

markFail : () ==> ()
markFail () == is not yet specified;

setMsg : seq of char ==> ()
setMsg (msg) == is not yet specified;

end TestRunner

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# VDM-SL test example

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testOdd: ()==>()
testOdd()== (
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  Assert `assertFalseMsg("Expected x " ^
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end MyTest
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## VDM-SL test example

# Java/JUnit4 translation

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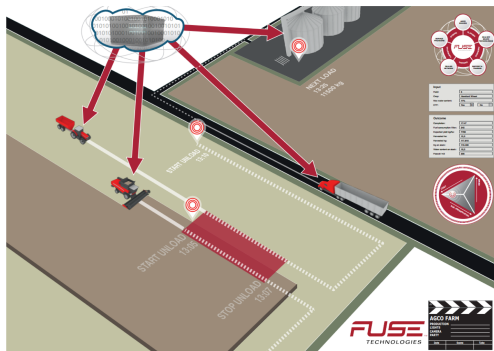
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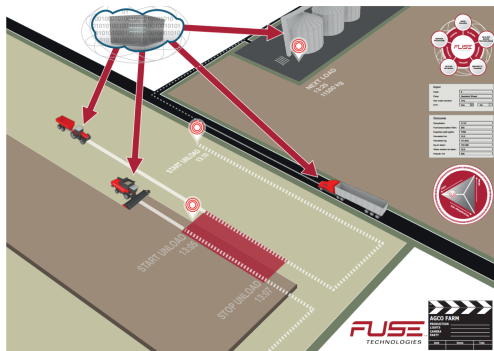
# Harvest planning case study

- Master algorithm in VDM
  - Code-generated to Java
- 4200 lines of VDM
  - $\approx$  3100 lines of model
  - $\approx$  1100 lines of tests
- 134 VDM-SL tests
  - VDM tests:  $\approx$  7 hours
  - Java versions:  $\approx$  30 min.



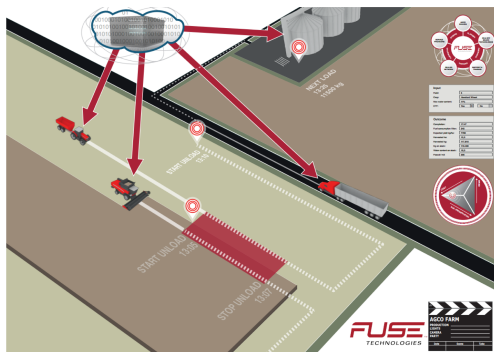
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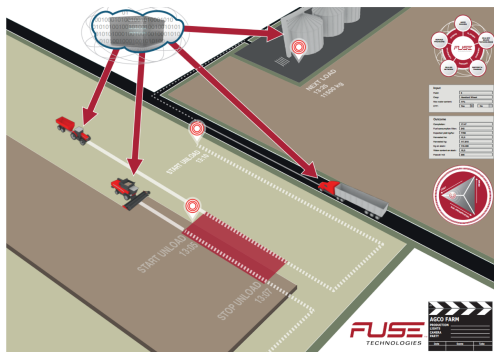
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# Jenkins integration

- Overture CLI extension
  - Pass property  
-Dvdm.unit.report
- VDM test reports
  - Generate XML reports
  - Jenkins visualisation

## Test Result : (root)

5 failures (+5)

134 tests (+0)

Took 2 hr 25 min.

[add description](#)

### All Failed Tests

Test Name	Duration	Age
<a href="#">FoulumTest.test_field_test_1_Headland_Bee</a>	1.2 sec	1
<a href="#">FoulumTest.test_field_test_1_OnTheGo_Bee</a>	1.8 sec	1
<a href="#">HobroLandevejTest.test_BCO_HeadlandUnload</a>	29 sec	1
<a href="#">HobroLandevejTest.test_BCO_OnTheGo</a>	38 sec	1
<a href="#">HobroLandevejTest.test_BCO_SP</a>	30 sec	1

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Class	Duration	Fail (diff)	Skip (diff)	Pass (diff)	Total (diff)
<a href="#">BeeTest</a>	7.3 sec	0	0	3	3
<a href="#">BridgeTest</a>	0.11 sec	0	0	4	4
<a href="#">FieldTest</a>	2.2 sec	0	0	2	2
<a href="#">FoulumTest</a>	1 min 34 sec	2 +2	0	18 -2	20
<a href="#">HeadlandUnloadTest</a>	20 sec	0	0	6	6
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# Thank you



**VDMUnit for VDM-SL:**

[https://github.com/overturetool/overture/  
pull/671](https://github.com/overturetool/overture/pull/671)